**Frontiers in Heat and Mass Transfer** 



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# ENHANCEMENT OF THERMOELECTRIC DEVICE PERFORMANCE THROUGH INTEGRATED FLOW CHANNELS

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## ABSTRACT

In this study, the thermoelectric performance of an integrated thermoelectric device (iTED) with rectangular, round end slots, and circular flow channel designs applied to waste heat recovery for several hot stream flow rates has been investigated using numerical methods. An iTED is constructed with p- and n-type semiconductor materials bonded to the surfaces of an interconnector with flow channels drilled through it. This interconnector acts as an internal heat exchanger directing waste heat from the hot stream to thermoelectric elements. The quantity of heat extracted from the waste heat source and the subsequent amount of electrical power generated P<sub>0</sub> from the iTED is increased significantly for the circular flow channels, followed by round end slots and rectangular flow channels, respectively. At Re = 100, the round end slots and the circular flow channels showed nearly 2.6 and 2.9 times increment in P<sub>0</sub>, and 1.5 and 1.65 times in  $\eta$  when compared to the rectangular flow channels values. Conversely, when Re is increased from 100 to 500, the iTED with rectangular flow channels showed 2.67- and 1.6-fold improvement in P<sub>0</sub> and  $\eta$ , respectively. However, the circular configurations showed 2.27- and 1.41-fold increases in P<sub>0</sub> and  $\eta$  values, respectively. Within the Re range studied, the inclusion of flow channels' pumping power in  $\eta$  calculations showed negligible effect. For an iTED with circular flow channels, an increase in a cold side convective heat transfer coefficient  $h_c$  resulted in an enhancement in  $P_0$  and  $\eta$  values. Besides a  $h_c$  effect, the heat loss to the ambient via convective and radiation heat transfer exhibited an increase in  $\eta$ .

Keywords: Circular, numerical model, performance, rectangular, slots, thermoelectrics, waste-heat recovery.

## 1. INTRODUCTION

Over the last two decades, the increase in human population and demand of a higher quality of life has increased the energy requirements enormously. Thus, the carbon foot-print and waste heat released into the atmosphere are proliferated from both energy consuming and producing equipment. Based on the DOE report by Smith and Thornton (2009), it is approximated that two-thirds of the supplied input energy to these systems is rejected as a waste heat to the atmosphere. Scavenging even a small percentage of waste energy (5-10%) can reduce negative environmental impacts and make current equipment more economical. Hence, we are compelled to explore environmentally-benign technologies that can replace and/or enhance the performance of current energy conversion systems while mitigating the emission of greenhouse gases. Thermoelectric devices (TEDs) are one of the prominent technologies to recover the waste heat and convert it into useful electricity. TEDs work as electric power generators when the junction of two different electrically and thermally conductive materials are exposed to a temperature differential (Rowe, 2006). TEDs have no moving parts, are noise-free, scalable, reliable, and compact. The broad range of applications of these devices include temperature measurement, remote radio and satellite power generation stations, refrigeration cooling and waste heat recovery from exhaust streams and other low-grade heat sources, pocket electronics, biothermal batteries to power pacemakers, localized cooling in electronic components and automobile seats.

The maximum conversion efficiency of a TED depends on both materials' intrinsic properties (figure of merit ZT, expressed as  $(\sigma \alpha^2 T)/\kappa$ ), here  $\sigma$ ,  $\alpha$ , T, and  $\kappa$  are the electrical conductivity, Seebeck coefficient, absolute temperature, and total thermal conductivity, respectively) and the hot and cold junction temperatures  $(T_h \text{ and } T_c)$  (Rowe, 2006). Therefore, the TED efficiency can be enhanced either through material strategies (via methods of nano-structuring and fabrication (Sootsman et al., 2009; Tritt, 2011; Biswas et al., 2012) and use of new bulk materials (Rowe, 2006; Poudel et al., 2008)) and/or through the system modeling, design, and optimisation (Caillat et al., 1999; Kaibe et al., 2005; Hodes, 2010; Crane et al., 2012). Caillat et al. (1999) achieved a TED efficiency of 15% using segmented thermoelectric legs. When cascading TEDs are subjected to  $T_h = 550 \ ^{\circ}C$  and  $T_c = 30 \ ^{\circ}C$ , Kaibe *et al.* (2005) reported an efficiency of 12.1%. Hodes (2010) investigated the optimum leg geometries for maximizing either thermoelectric efficiency or power output in the absence and then presence of interface contact resistances. Recently, Crane et al. (2012) built a full-scale cylindrical-shaped thermoelectric generator using segmented and high-power density elements and obtained a maximum power output of 608 W. They also demonstrated the validity of steady-state and transient TED models by constructing them on cylindrical gas/liquid heat exchangers. Further, Yazawa and Shakouri

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(2012) studied the co-optimisation of TEDs by considering the TE leg shape, heat sink, load and contact resistances, and heat losses.

It has been demonstrated that the three-dimensional 3D thermoelectric modeling substantially influences the overall performance of TED (Harris et al., 2006; Hu, 2009; Ziolkowski et al., 2010; Chen et al., 2011). Harris et al. (2006) studied the temperature and electrical field distributions in TEDs using a finite volume numerical model by taking into account both temperature-dependent material properties and contact resistances at the interfaces of the thermoelectric material and conducting shoes. Hu (2009) analysed the characteristics of two compact gasphase heat exchangers placed on the hot and cold sides of the TED to enhance heat transfer and fluid flow while minimizing thermal stresses. Ziolkowski et al. (2010) investigated the performance of TEDs including the parasitic effects such as convection, radiation and conduction heat bypass. Further, accounting all temperature-dependent material properties and non-linear fluid-thermal-electric coupled mechanisms, Chen et al. (2011) developed a 3D numerical model for TEDs in FLUENT-UDS environment. Recently, using 3D numerical methods Reddy et al. (2012, 2013a) studied the enhanced heat transfer characteristics and performance of composite and integrated TEDs applied to waste heat recovery systems.

It is observed from the literature that the conventional TED designs, due to their geometrical structure, induce large amounts of thermal stresses. Furthermore, a thermal resistance present at the interfaces of the ceramic plates and the heat sinks. Keeping in mind the minimisation of contact resistances and thermal stresses, recently Reddy et al. (2012, 2013b) proposed an integrated thermoelectric device (iTED) where each leg is made of semiconductor slices bonded onto a highly conducting inter-connector material with flow channels which act as a heat exchanger between the flowing fluid and thermoelectric materials. It was demonstrated that such a design would increase the performance of iTED with given thermoelectric materials and geometric conditions. Furthermore, the iTED's interconnector flow channel configuration type will play a significant role in achieving higher power output and efficiencies. Therefore, in this study, the theoretical performance of an iTED applied to waste heat recovery with various integrated flow channel designs (rectangular, round end slots and circular) and several hot fluid flow rates have been investigated. Further, the influence of cold wall convective heat transfer conditions and the heat loss to the ambient via the convective and radiation heat transfer on iTED's performance are studied.

## 2. GEOMETRY, GOVERNING EQUATIONS, AND SOLUTION PROCEDURE

Thermoelectric legs made of semiconductor slices bonded onto a highly conducting inter-connector material with a flow channel configuration can be treated as integrated Thermoelectric Devices (iTED). The schematic of such an iTED with a rectangular flow channel configuration is shown in Fig. 1a. It comprises of a vertical leg constructed with p- and n-type semiconductor materials and copper as a inter-connector. Leg with square cross-section  $W \times D$  and height H is considered. Further, the thickness of n- and p-type semiconductor slices equal in size d is assumed. As shown in Fig. 1a, the inter-connector with rectangular flow channel grooved through it acts as a heat exchanger between n- and p-type semiconductor slices. The two dimensional view (in the direction of flow) of various flow channel configurations such as rectangular, slots with round end and circular channels are also shown in Fig. 1b. The two copper connectors placed one at the top and one at the bottom of the leg constitute two junctions. The connectors also act as terminals when the device is connected to the load circuitry of resistance  $R_L$ .

As shown in Fig. 1a, a hot fluid with constant inlet temperature  $T_{in}$ and uniform velocity U enters the main flow channel of cross-sectional area  $D \times (L - 2d)$ , flows through inter-connector's flow channels, and leaves at the exit of the fluid domain. The inner walls and left and right side surfaces of inter-connector are solely accountable for the heat trans-



Fig. 1 Schematic of a) an integrated thermoelectric device with b) various flow channel configurations and c) their computational mesh.

fer from the hot fluid to the thermoelectric (TE) elements. The top surface of the upper connector and the lower surface of the bottom connector are exposed to either constant cold temperature  $T_c$  or convective heat transfer conditions. The remaining surfaces of the iTED are subjected to either adiabatic or convective and radiative heat transfer boundary conditions while keeping the main flow channel walls adiabatic. An upstream buffer length 5W and a downstream buffer zone 20W have been considered to eliminate thermal back diffusion at the inlet and to meet an outflow boundary condition at the outlet of the flow domain, respectively. The iTED connected to a load resistance  $R_L$  delivers electrical power via the Seebeck voltage produced through a temperature difference  $T_h - T_c$  between the inter-connector channel walls and the cold surface.

The mass, momentum, and heat transport in the fluid regions and the electric current flow and heat transport in the TE materials are governed by the three-dimensional conservation equations subjected to the following assumptions: the thermophysical properties of the fluid are kept constant; the fluid flow is steady, laminar and incompressible; the materials are heterogeneous, isotropic, and the thermoelectric properties are temperature-dependent.

The governing equations for the incompressible fluid flow and heat transport in the fluid region and the thermo-electric coupling effects in the conductor and semiconductor materials under steady-state conditions are written as:

• Continuity equations:

$$\nabla \cdot \mathbf{v} = 0 \quad \text{fluid} \tag{1}$$

$$\nabla \cdot \mathbf{J} = 0$$
 conductor and semiconductor (2)

• Momentum equation:

$$\rho_f \left( \mathbf{v} \cdot \nabla \mathbf{v} \right) = -\nabla P + \mu \nabla^2 \mathbf{v} \tag{3}$$

• Energy equations:

$$(\rho c_P)_f (\mathbf{v} \cdot \nabla T) = \nabla \cdot (k_f \nabla T)$$
 fluid (4)

$$\nabla \cdot \left(k\nabla T\right) + \rho \mathbf{J}^2 = 0 \quad \text{conductor} \tag{5}$$

$$\nabla \cdot \left( k \nabla T \right) + \rho \mathbf{J}^2 - T \mathbf{J} \cdot \left[ \left( \frac{\partial \alpha}{\partial T} \right) \nabla T + (\nabla \alpha)_T \right] = 0 \quad \text{semiconductor} \quad (6)$$

In the left-hand side of Eq. (6), the second, third, and fourth terms represent the Joule heating, Thomson effect, and Peltier cooling respectively.

The total electric potential calculated using the non-ohmic currentvoltage (Domenicali, 1954) relationship is written as:

$$\nabla V = \nabla V_O + \nabla V_S = -\rho \mathbf{J} - \alpha \nabla T \tag{7}$$

The first and second terms in Eq. (7), denotes the electrostatic and the Seebeck potential distributions, respectively. Here, the electrostatic potential is due to current flowing in the device whereas the Seebeck potential is due to the temperature differential created in the thermoelectric material.

The associated thermal and electrical boundary conditions for Eqs. (1) to (7) with respect to the geometry shown in Fig. 1a are, at terminal 'in':  $J = \frac{V_0}{A_{\xi}(R_i+R_L)}, \frac{\partial T}{\partial \xi} = 0$ ; at terminal 'out':  $V = 0, \frac{\partial T}{\partial \xi} = 0$ ; at the main flow channel walls:  $\frac{\partial T}{\partial \xi} = 0$ ; at fluid flow inlet:  $u = U, v = w = 0, T = T_{in}$  and at fluid flow outlet:  $\frac{\partial^2 u}{\partial \xi^2} = \frac{\partial^2 T}{\partial \xi^2} = v = w = 0$ . At the interface between the semiconductor and connector or inter-connector materials, the continuity of temperature, current density and the heat flux conditions are imposed (Reddy *et al.*, 2012).

In the above paragraph,  $R_i$  is the total internal resistance due to nand p-type slices, connectors and inter-connector materials, and  $R_L$  is the external load resistance.  $V_0$  is the total built-in open circuit voltage of iTED at no-load condition and it is calculated as the summation of the Seebeck potential at the interfaces of the semiconductor and conductor materials. The Seebeck voltages are evaluated with reference to the hot surface temperature at the interface of the semiconductor and interconnector materials. The total  $R_i$  and  $V_0$  of the device are evaluated as:

$$R_{i} = \sum_{j=n,p,c,ic} \frac{L_{j}}{A_{j}} \left[ \frac{1}{V_{j}} \int_{V_{j}} \rho_{j} dv_{j} \right] \text{ and }$$
$$V_{0} = \sum_{j=n,p} \frac{1}{As_{j}} \int_{As_{j}} |\alpha_{j}| \frac{dT}{d\xi} dAs_{j}.$$
(8)

The boundary conditions at the top surface of the upper connector and the bottom surfaces of the lower connectors:

$$T = T_c$$
 or  $q'' = h_c(T - T_\infty)$ , and  $\frac{\partial V}{\partial \xi} = 0$ , (9)

and at all other iTED surfaces exposed to surroundings:

$$q^{\prime\prime} = \frac{\partial T}{\partial \xi} = 0 \text{ or } q^{\prime\prime} = h_s(T - T_\infty) + \epsilon \sigma (T^4 - T_{sur}^4) \text{ and } \frac{\partial V}{\partial \xi} = 0.$$
(10)

Here,  $\xi$  denotes the direction normal to the corresponding surface.

The pressure drop  $\Delta P/L$ , power output  $P_0$ , heat input  $Q_h$  at the inter-connector walls exposed to hot fluid and the thermoelectric conversion efficiency of iTED are evaluated as:

$$\frac{\Delta P}{L} = \frac{1}{25W} \left[ \int_0^{H-2d} \int_0^D Pdydz |_{x=0} - \int_0^{H-2d} \int_0^D Pdydz |_{x=25W} \right],\tag{11}$$

$$P_0 = I^2 R_L, \tag{12}$$

$$Q_h = -\frac{1}{As} \int_{As} k_f \frac{\partial T}{\partial \xi} dAs, \qquad (13)$$

and

$$\eta = \frac{P_0}{Q_h}.$$
(14)

Using the finite volume formulation of Eqs. (1) to (6) and the constitutive relation (Eq. 7) along with the above mentioned associated boundary conditions, the numerical simulations are performed in the FLUENT-UDS (User Defined Scalar) software. The coupling between velocity and **Table 1** Grid independence study for iTED with various flow channel<br/>configurations (grid size in bold face is chosen for further simula-<br/>tions) [ $T_{in} = 550$  K,  $T_c = 300$  K, d = 5 mm, and H = 20 mm].

Rectangular duct [ $Re = 400, R_L = 4.69 \times 10^{-3} \Omega$ ]							
		% error		% error		% error	
Cells	$P_0, \mathbf{W}$	in $P_0$	$Q_h, \mathbf{W}$	in $Q_h$	$\eta, \%$	in $\eta$	
300124	0.0739		1.6977		4.3513		
633600	0.0724	1.998	1.6821	0.929	4.3057	1.060	
1139152	0.0714	1.445	1.6714	0.642	4.2716	0.797	
2000922	0.0720	0.859	1.6803	0.534	4.2857	0.327	
Multiple slots with rounded end [ $Re = 400, R_L = 5.12 \times 10^{-3} \Omega$ ]							
598200	0.1593		2.6052		6.1140		
1175504	0.1590	0.151	2.6027	0.097	6.1107	0.054	
2272752	0.1595	0.288	2.6137	0.420	6.1026	0.133	
3307802	0.1583	0.739	2.6029	0.415	6.0830	0.323	
Circular ducts [ $Re = 500, R_L = 5.29 \times 10^{-3} \Omega$ ]							
948960	0.2008		3.0114		6.6675		
1822976	0.1983	1.259	2.9919	0.653	6.6276	0.602	
3723840	0.1978	0.230	2.9901	0.058	6.6162	0.172	

pressure is handled by using the SIMPLE algorithm. The convective and diffusive terms are discretised with a power law scheme and the pressure term is handled with a standard scheme. The geometric models and mesh are generated in Gambit 2.4. The Seebeck and Ohmic potential distributions (Eq. (7)) and continuity of current density (Eq. (2)) are evaluated using UDS fields. The Ohmic heating, Peltier and Thomson effects are modeled as source terms in the energy equation (Eq. (6)). The electric current is calculated based on the open-circuit Seebeck voltage produced at a given load resistance  $R_L$ . Additional details on implementation of numerical model for TED in the FLUENT-UDS environment has been given in the articles (Chen et al., 2011; Reddy et al., 2012). The convergence criteria for mass and momentum, energy, and current density, Seebeck and Ohmic electric potentials are set as  $10^{-5}$ ,  $10^{-15}$ , and  $10^{-10}$ , respectively. After performing a grid independence study, suitable grid sizes were chosen for various integrated flow channel configurations and these are depicted in Table 1. The orthogonal, nonuniform grid is used, and for brevity, the computational mesh for an iTED along the front and side views is shown in Fig. 1c. Furthermore, the thermoelectric model has been validated with published results and the details are given in our earlier works (Reddy et al., 2012, 2013a).

### 3. RESULTS AND DISCUSSION

The performance of an integrated thermoelectric device (iTED) for various flow channel configurations in terms of power output  $P_0$ , heat input  $Q_h$ , conversion efficiency  $\eta$ , electric current I, Seebeck and Ohmic voltages V, and pressure drop  $\Delta P/L$  with several hot fluid flow rates  $(50 \le Re \le 500)$  has been investigated using numerical methods. The rectangular, round end slots, and circular flow channels within the interconnector acting as an internal heat exchanger for the hot fluid flow are considered in the present study. Further, the effects of cold surface boundary condition (constant temperature and convective heat transfer conditions) on the performance of iTED with circular flow channel configuration alone has been studied when the remaining surfaces, except the flow channel walls, exposed to either adiabatic or convective and radiation conditions. The iTED made of n-  $(75\% Bi_2Te25\% Bi_2Se)$  and p-type ( $25\% Bi_2Te_3 75\% Sb_2Te_3$  (1.75% excess Se)) semiconductor materials, copper as connector and interconnector, and the air as hot fluid have been considered. The thermo-electrical properties of the semiconductor and conductor materials are varied with temperature (Reddy et al., 2012), and the thermo-physical properties of air are kept constant. Fur-



Fig. 2 The distributions of x-velocity, temperature, Seebeck and Ohmic voltage for various integrated flow channel configurations A) rectangular B) slots with round end and C) circular.

thermore, to achieve maximum  $P_0$  from an iTED, the load resistance  $R_L$  equaling to the total internal resistance  $R_i$  of the device (Eq. (8)) is used in the numerical simulations.

The geometrical dimensions: height H = 20 mm, depth D = 5 mm, connector thickness t = 1.5 mm and length L = 10 mm, the semiconductor and inter-connector width W = 5 mm, and semiconductor slice thickness d = 5 mm are maintained constant in the reported results. The hot fluid inlet temperature  $T_{in} = 550K$  and cold wall temperature  $T_c = 300K$  are invariant. Further, the ratio of flow cross-sectional areas of an integrated fluid flow channels and the main flow channel is kept fixed and it is expressed as

$$\phi_{FA} = \frac{A_c}{A} \tag{15}$$

Here, A and  $A_c$  are the cross-sectional areas of main flow channel  $D \times (H - 2 \times d)$  and the integrated flow channels, respectively. For various flow channel configurations as shown in Fig. 1b, the  $A_c$  is evaluated as

$$A_{c} = \begin{cases} a \times b & \text{Rectangular} \\ n_{s} \left[ a_{s} \times b_{s} + \frac{1}{4} \pi a_{s}^{2} \right] & \text{Slots with round end} \\ n_{c} \pi r^{2} & \text{Circular.} \end{cases}$$
(16)

In Eq. (16),  $n_s$ ,  $n_c$ , and r denote the number of slots, number of circles, and the circle radius, respectively. In the present study,  $n_s = 3$  and  $n_c = 6 \times 3$  are used. While maintaining  $\phi_{FA} = 0.283$  invariant, the dimensions of the various flow channel designs are calculated using Eq. (16).

X-velocity, temperature, and Seebeck and Ohmic voltage distributions for integrated rectangular, round end slots, and circular flow channel configurations with hot fluid flow rate Re = 300 are shown in Fig. 2, whereas the same variable contours for circular flow channels alone with two Re values are depicted in Fig. 3. The x-velocity and temperature distributions are depicted on z-plane sectioned at 2.5 mm while the Seebeck and Ohmic voltage contours are shown on x-plane at 27.5 mm. Further, to show velocity and temperature contours in Figs. 2 and 3, an up-stream and down-stream lengths of W and 5W, respectively have been taken. At  $\phi_{FA} = 0.283$  and a given Re value, it is observed that the flow channel



Fig. 3 The contours of a) x-velocity b) temperature c) Seebeck and d) Ohmic voltages for integrated circular flow channels at Re = 100and 500 values.

type has a substantial effect on fluid flow, heat transfer, and electric characteristics of iTED. As seen from Fig. 2, in the integrated flow channels, the fluid's average velocity increases and the bulk temperature decreases as the channel configuration varies from rectangular to circular and this subsequently resulted in enhanced heat transfer to the thermoelectric elements. The reason for enhanced heat transfer is due to the increase in the flow channel's surface area with decrease in size of the flow channels. For instance, for the flow configurations studied here, the surface area of round end slots and circular flow channels are 2.16 and 3.12 times higher when compared to the rectangular flow channels, respectively. Furthermore, as shown in Fig. 3 for the circular flow channels, an increment in temperature gradients near the flow channel walls is resulted in an increase in Re from 100 to 500. This increase in temperature gradients promotes higher convective heat transfer rates and thus in turn results in a reduction of iTED's total thermal resistance value. The recirculation zones and the flow patterns formed in the down stream of integrated flow channels have negligible influence on the thermo-electric performance of iTED, however they exhibit considerable effect on the total pressure drop experienced in their respective channels.

It is seen that an increase in heat transfer area of the flow channels either through round end slots or circular channels as well as the increase in *Re* resulted in larger temperature gradients in the thermoelectric (TE) elements. As shown in Figs. 2 and 3, this increase in temperature gradients generates higher Seebeck voltages in the TE elements. In each TE element, the Seebeck voltage drop is calculated with respect to the copper material. However, the Ohmic voltage distribution can be obtained through the produced electric current I when the iTED terminals are connected to load circuitry of resistance  $R_L$ . At Re = 300, the 2.16 and 3.12 times increase in heat transfer area of slots with round end and circular flow channels, respectively, results in 1.6 and 1.74 times increase in Seebeck voltage values when compared to the rectangular flow channel's value. Furthermore, as the flow channel configuration changes from rectangular to circular, the Ohmic voltage drop increased from 0.0168 to 0.0299 V; on the other hand, as Re increased from 100 to 500 for the circular flow channel, the Ohmic voltage drop raised from 0.0206 to 0.0333. It is also noticed that with  $R_L = R_i$  condition, the Seebeck co-



Fig. 4 Effects of *Re* on a) heat input and pressure drop and b) power output and thermal efficiency for flow channel configurations.

efficients and temperature gradients are only responsible for the Ohmic voltage drop variations and it is independent of  $R_i$  values.

The response of total internal resistance  $R_i$  and produced electric current I and Seebeck and Ohmic voltages V with Re for various integrated flow channel configurations is presented in Figs. 4a and b, respectively. The rise in temperature gradients in thermoelectric elements with an increase in Re produces larger Seebeck voltages and thus Ohmic voltages (as seen in Fig. 4a). Irrespective of Re value, the circular flow channels showed higher V-I values, followed by round end slots and rectangular flow channels, respectively. It is noticed that I and V exhibited similar trend with Re. At a given Re, the Ohmic voltage drop is higher than the Seebeck voltage drop value. Furthermore, from Fig. 4b, the Rein the specified range  $50 \le Re \le 500$  has minimal effect on  $R_i$  predictions and this is attributed to variation of electrical resistivity behaviour with temperature. However, at a given Re value, a minor increment in  $R_i$ is noticed when the iTED's flow channels are varied from rectangular to circular flow configurations.

Figures 5a and b show the results of heat input  $Q_h$  and pressure drop  $\Delta P/L$ , and power output  $P_0$  and thermal efficiency  $\eta$ , respectively with Re for different integrated flow channel configurations. Irrespective of the flow channel design type, an increase in Re shows an enhancement in  $Q_h$ ,  $\Delta P/L$ ,  $P_0$  and  $\eta$  predictions. The increase in Re promotes higher heat transfer rate from the hot fluid to TE elements  $Q_h$  and this subsequently assists in achieving larger Seebeck voltages (Fig. 4b) and thus results in higher  $P_0$  and  $\eta$  (as seen in Fig. 5b) values. For instance, at Re = 500 the rectangular flow channel configuration showed 1.68-, 2.6-, and 1.6 times increment in  $Q_h$ ,  $P_0$  and  $\eta$ , respectively when compared to the values at Re = 100. Conversely, the round end slots and circular flow channel configurations produced 1.55-, 2.17-, and 1.4 fold, and 1.61-, 2.27-, and 1.41 times increase  $Q_h$ ,  $P_0$  and  $\eta$ , respectively at Re = 500 in comparison to values at Re = 100. For a given integrated



**Fig. 5** Effects of *Re* on a) internal resistance and b) electric current, Seebeck and Ohmic voltages for flow channel configurations.

flow channel configuration, the pressure drop  $\Delta P/L$  measured using the Eq. (11) is shown a non-linear behaviour with Re and it is due to the inclusion of additional pressure drop anticipated from the recirculation zones and flow patterns formation in the down-stream and up-stream of integrated flow channels. At given Re value, the circular flow channel configuration displayed higher  $\Delta P/L$ , and the rectangular flow channel configuration showed the lower  $\Delta P/L$  value. It is observed that when Re is less than 200, it has significant effect on device characteristics. It is due to the mutual interplay of temperature gradients and the thermo-electrical property dependency with temperature. It can also noticed that within the Re range, the consideration of pumping power in the  $\eta$  calculations has negligible effect.

It is inferred that the iTED with circular flow channel configuration always performs better when compared to other two configurations, i.e. round end slots and rectangular channels. Further, at given *Re* values, the discrepancy between the performance parameters of circular and round end slots flow channels is less when compared to the discrepancy between the circular and rectangular flow channel configurations values. However, this discrepancy increases minimally with an increase in *Re*. It is also surmised that the number of times increase in integrated heat transfer surface area of channels via round end slots and circular flow channels is not proportional to the increase in the performance parameters.

Figure 6 depicts the effects of cold side convective heat transfer coefficient  $h_c$  ( $10 \le h_c \le 10^3$ ) on  $Q_h$ ,  $R_i$ ,  $P_0$ ,  $\eta$ , I and V for an iTED with circular flow channel configuration. In Fig. 6, the data with filled bullets represents when the iTED walls (except the cold walls and the main flow channel walls) are subjected to adiabatic condition ( $h_s = 0$ ). For given hot fluid flow rate Re = 500 and cold fluid temperature  $T_{\infty} =$ 300 K, A rise in  $h_c$  shows an improvement in  $Q_h$ ,  $P_0$ ,  $\eta$ , I and V values. However, the  $R_i$  drops as  $h_c$  increases. It is noticed that as  $h_c$  increases the cooling heat transfer rate also increases. Therefore, to achieve energy conservation, the heat transfer from hot fluid to iTED also increases



Fig. 6 The influence of cold side convective heat transfer coefficient  $h_c$  on a) heat input and internal resistance b) power output and thermal efficiency and c) electric current, Seebeck, and Ohmic voltages.

and this results in higher temperature gradients in TE elements. Further, this increase in temperature gradients showed an enhancement in iTED performance parameters. It is also shown in Fig. 6 the influence of convective and radiation heat transfer  $h_s$  (0, 5, 10, and 20 Wm<sup>-2</sup>K<sup>-1</sup>) on iTED performance at  $h_c = 10 \text{ Wm}^{-2}\text{K}^{-1}$  (the data with empty bullets). Here, the surrounding temperature  $T_{sur} = 300 \text{ K}$  is fixed and the emissivities for the semiconductor and the highly polished copper conductor are taken as 0.45 and 0.03, respectively. It is observed that the rise in  $P_0$  and  $Q_h$  and decrease in  $\eta$  with an increase in  $h_s$  values. The heat loss from the walls exposed to ambient resulted in larger  $Q_h$  predictions; however, the decrease in  $R_i$  and larger temperature gradients showed enhancement in  $P_0$  values.

#### 4. CONCLUSIONS

The performance of an integrated thermoelectric device (iTED) with various flow channel (rectangular, round end slots, and circular) configurations acting as internal heat exchangers between hot fluid and thermoelectric elements for different flow flow rates Re has been investigated using numerical simulations. Further, the influence of cold wall convective heat transfer conditions and the heat loss to the ambient via the convective and radiation heat transfer on iTED's performance with circular flow channel configuration has been studied.

It is observed that the integrated flow channel configuration type and Re have a substantial effect on iTED's performance. At a given flow channel cross-sectional areas ratio  $\phi_{FA} = 0.283$ , irrespective of Revalue, an iTED with circular flow channel configuration showed higher power output P<sub>0</sub>, efficiency  $\eta$ , and pressure drop values, followed by round end slots and then rectangular configurations, respectively. This behaviour is attributed to increase in flow channels' surface area with decrease in channel size.

The 2.16 and 3.12 fold increase in heat transfer surface area of the round end slots and the circular flow channels, respectively resulted in 1.6 and 1.74 times increase in Seebeck voltages, 2.36 and 2.72 times in P<sub>0</sub>, and 1.48 and 1.56 times in  $\eta$  values when compared to the rectangular flow channel configuration values at Re = 300 with inlet temperature  $T_{in} = 550K$  and cold surface temperature  $T_c = 300K$  conditions.

When Re increased from 100 to 500, the rectangular flow channel configuration showed 2.67- and 1.6-times improvement in P<sub>0</sub> and  $\eta$ , respectively. However, the round end slots and circular flow channels depicted 2.17- and 1.4 fold, and 2.27- and 1.41-fold increase in P<sub>0</sub> and  $\eta$ values. For given Re and flow channel configuration, the produced current and Seebeck and Ohmic voltages exhibited similar behaviour as that of P<sub>0</sub> and  $\eta$ . Due to mutual interplay of temperature gradients in thermoelectric material and the temperature dependency of the thermo-physical properties, the discrepancy in the performance of circular and slots with round end flow channels is less when compared to the discrepancy values between the circular and rectangular flow channel configurations.

For an iTED with circular flow channel configuration when the walls except the cold walls and the main flow channel walls are exposed to adiabatic conditions ( $h_s = 0$ ), an increase in  $h_c$  resulted in enhancement in heat output  $Q_h$ ,  $P_0$ , and  $\eta$  values. In addition to  $h_c$  effect, the rise in  $h_s$  (heat loss to ambient) showed an increase in  $P_0$  and  $Q_h$  and decrease in  $\eta$  values.

It has shown that under given set of operating conditions, the iTED with circular flow configuration performed better when compared to round end slots and rectangular flow channel configurations. Furthermore, within in the *Re* range studied, the consideration of flow channel pumping power in  $\eta$  evaluations has showed negligible effect.

### NOMENCLATURE

A	cross-sectional area (mm <sup>2</sup> )
$A_s$	surface area (mm <sup>2</sup> )
$c_P$	specific heat of fluid $(J/kg \cdot K)$
d	size of semiconductor material (mm)
D	depth of the thermoelectric leg (mm)
$D_h$	hydraulic diameter of main flow channel $\left(\frac{4(L-2d) \times D}{2(L-2d+D)}\right)$ (mm)
h	convective heat transfer coefficient $(W/m^2 \cdot K)$
H	height of the leg (mm)
Ι	electric current (A)
J	electric current density $(A/m^2)$
J	current density vector
k	thermal conductivity $(W/m \cdot K)$
L	length (mm)
N	number of thermoelectric modules
P	pressure $(N/m^2)$
$P_0$	power output $I^2 R_L$ (W)
$q^{"}$	heat flux $(W/m^2)$
Q	heat transfer (W)
R	electric resistance $(\Omega)$
Re	Reynolds number $ ho UD_h/\mu$

T	temperature (K)				
U	inlet velocity $(m/s)$				
v	velocity vector				
V	voltage (V)				
u, v, w	velocities in x,y,z directions (m/s)				
W	width (mm)				
x, y, z	coordinates (mm)				
Greek Symbols					
$\alpha$	Seebeck coefficient $(V/K)$				
$\mu$	dynamic viscosity $(N \cdot s/m^2)$				
ρ	electrical resistivity $(\Omega \cdot m)$				
$ ho_f$	density of fluid $(kg/m^3)$				
$\eta$	conversion efficiency				
Subscripts					
c	cold wall/conductor				
in	inlet				
f	fluid				
h	hot wall				
i	internal/integrated				
ic	inter-connector				
L	load				
n	n-type semiconductor				
0	Ohmic potential				
р	p-type semiconductor				
S	Seebeck potential				
surr	surroundings				
$\infty$	ambient				

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